

# Abstracts

## A 15 GHz Single-Stage GaAs Dual-Gate FET Monolithic Analog Frequency Divider with Reduced Input Threshold Power (Dec. 1988 [T-MTT])

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*K. Kanazawa, M. Hagio, M. Kazumura and G. Kano. "A 15 GHz Single-Stage GaAs Dual-Gate FET Monolithic Analog Frequency Divider with Reduced Input Threshold Power (Dec. 1988 [T-MTT])." 1988 Transactions on Microwave Theory and Techniques 36.12 (Dec. 1988 [T-MTT] (1988 Symposium Issue)): 1908-1912.*

A 15 GHz single-stage GaAs dual-gate FET monolithic analog frequency divider with a reduced input threshold power has been designed and fabricated. Use of the dual-gate structure for the FET mixer contributed to a simplified circuit configuration. Introduction of the rejection filter at the output port resulted in a reduction of the input threshold power to 1.4 dBm.

 [Return to main document.](#)